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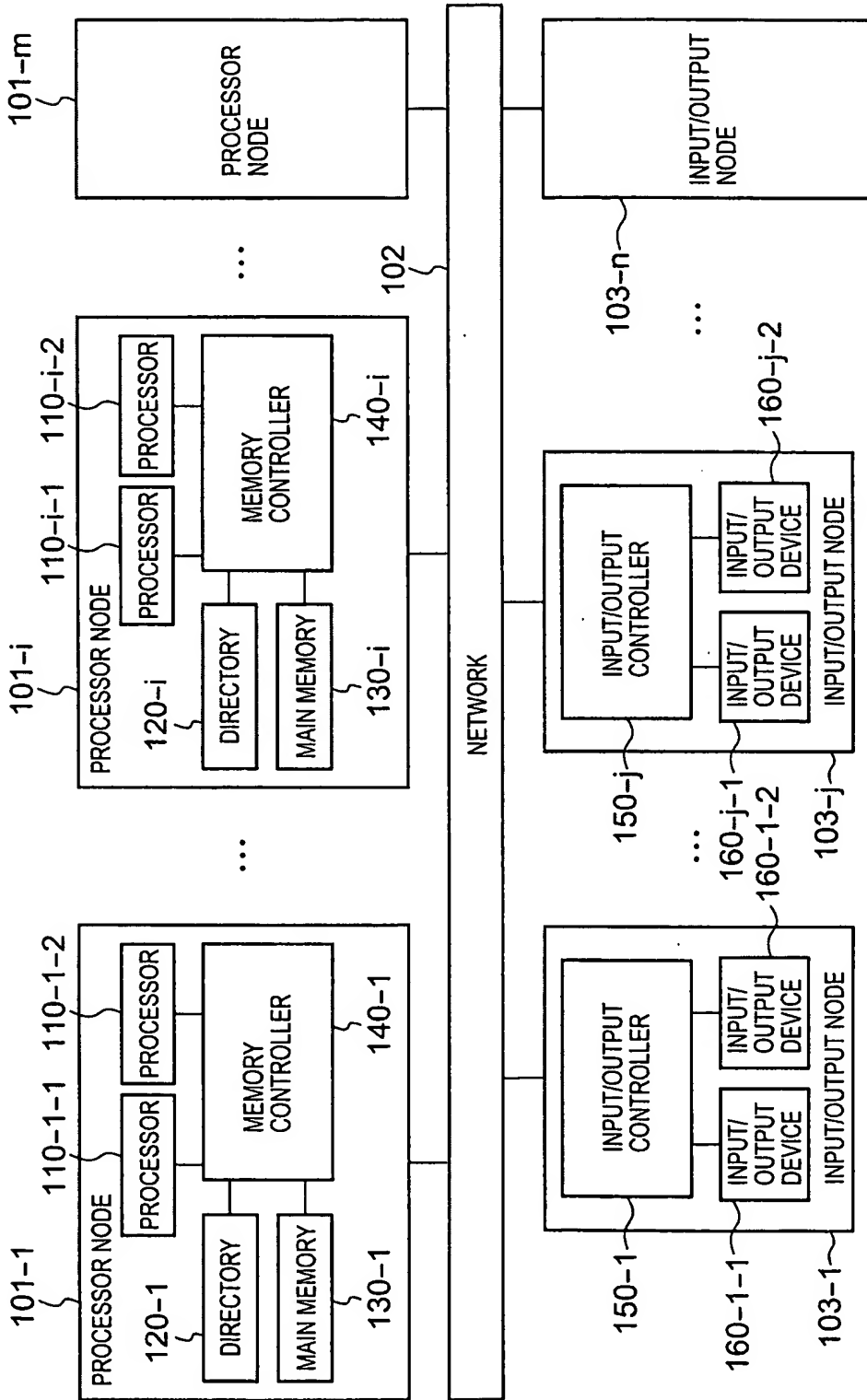


FIG. 1

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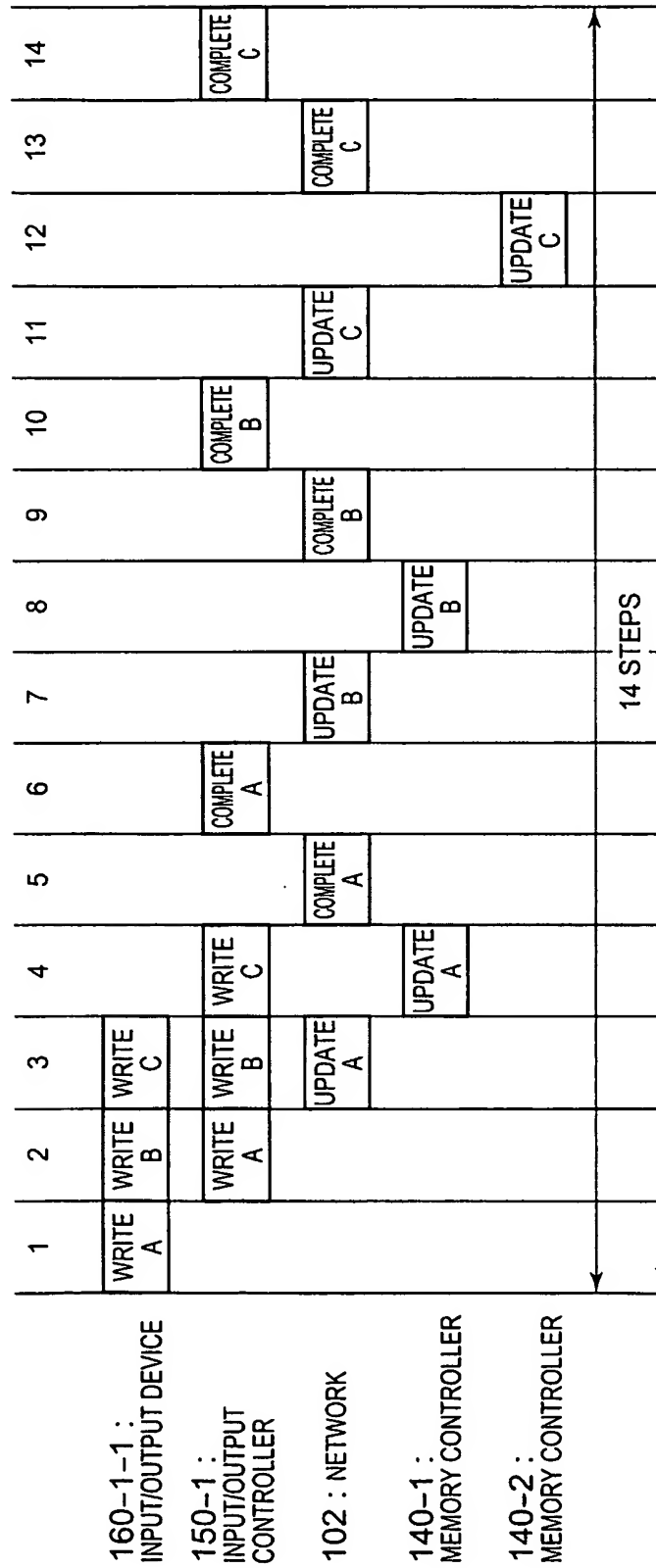


FIG. 2

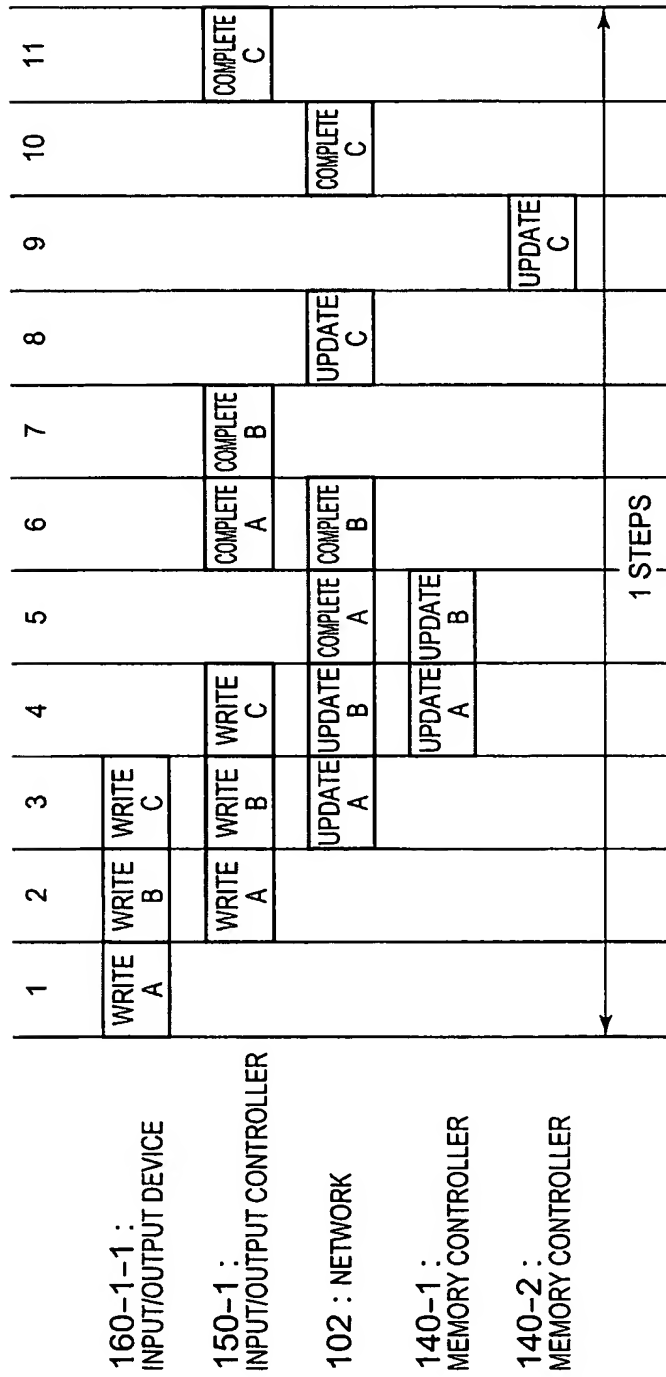


FIG. 3

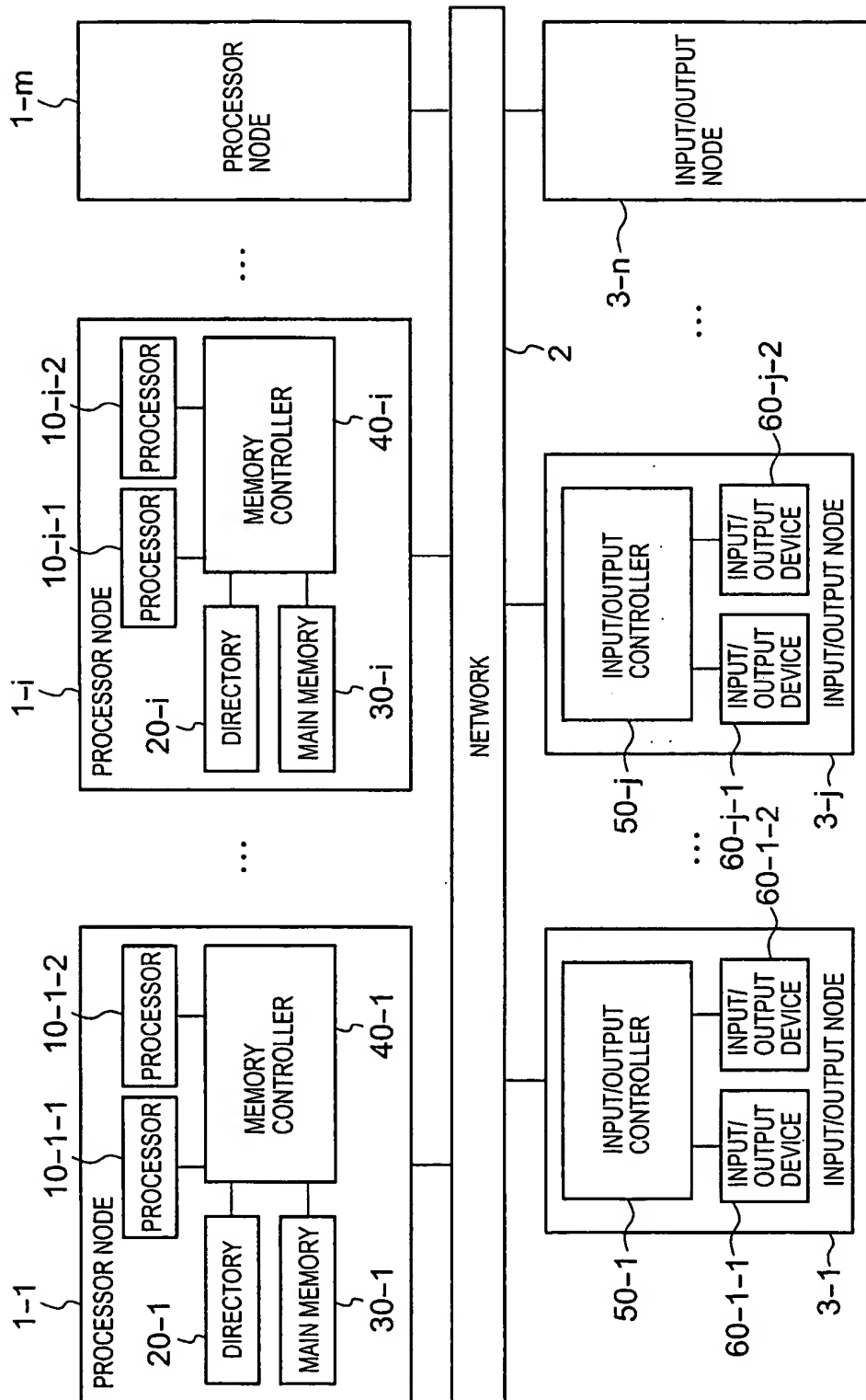


FIG. 4

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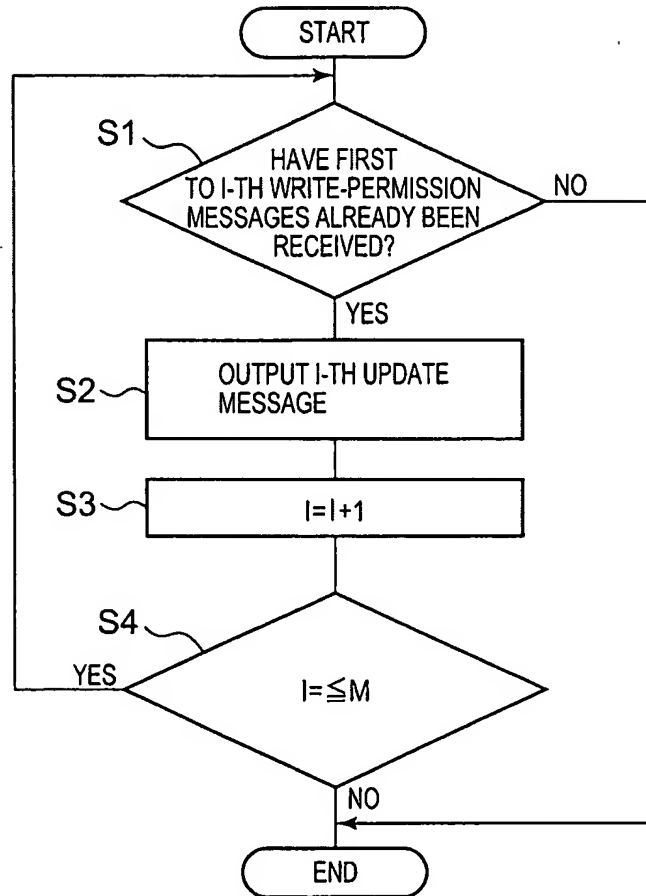


FIG. 5

60-1-1:
INPUT/OUTPUT DEVICE

50-1: INPUT/OUTPUT CONTROLLER

2 : NETWORK

40-1 : MEMORY CONTROLLER

STATE OF DATA A STORED IN DIRECTORY 20-1

STATE OF DATA B STORED
IN DIRECTORY 20-1

40-2 : MEMORY CONTROLLER

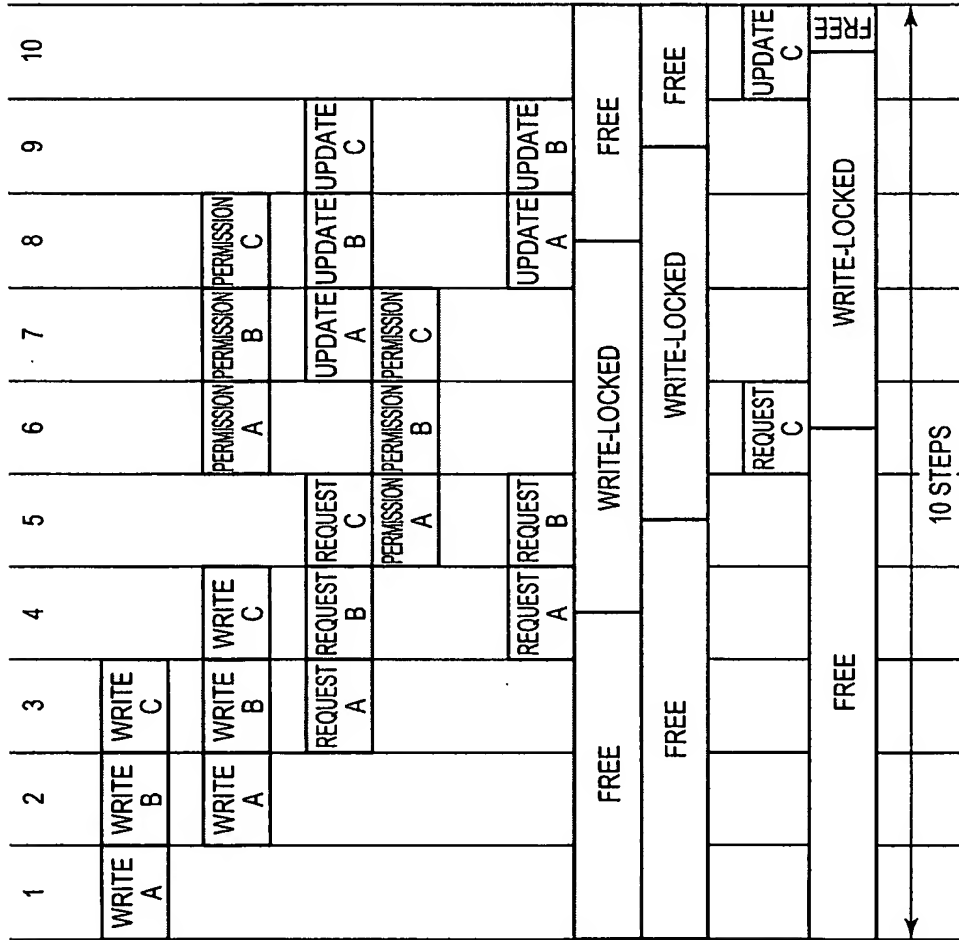
STATE OF DATA C STORED
IN DIRECTORY 20-2

Fig. 6

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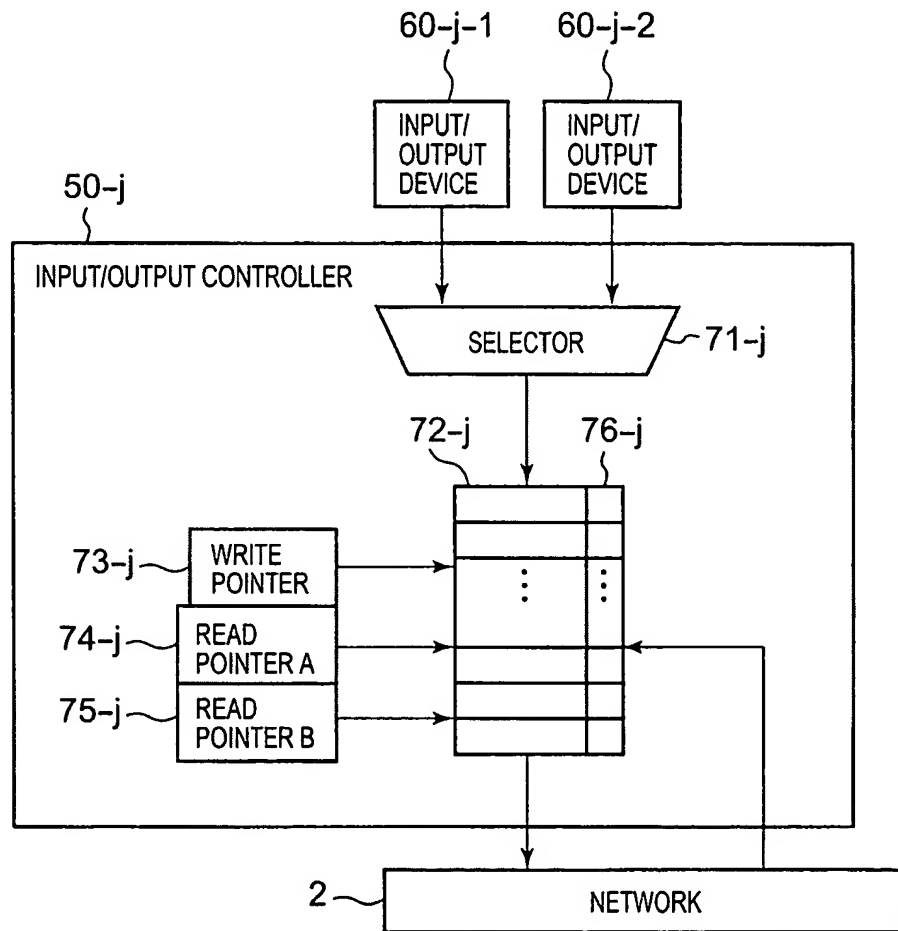


FIG. 7

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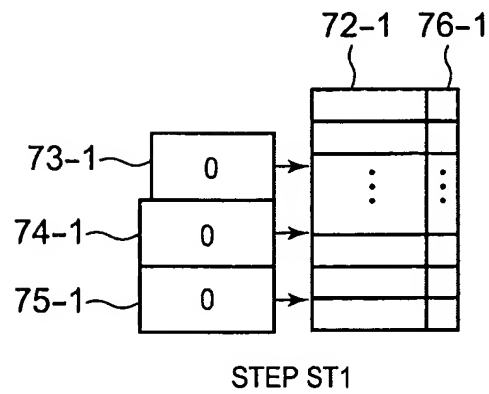


FIG. 8A

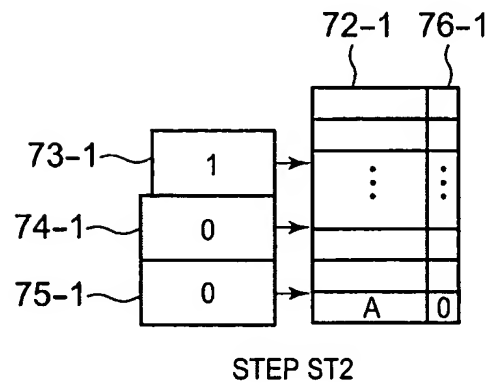


FIG. 8B

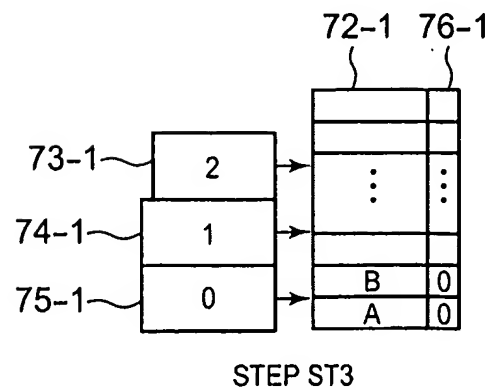


FIG. 8C

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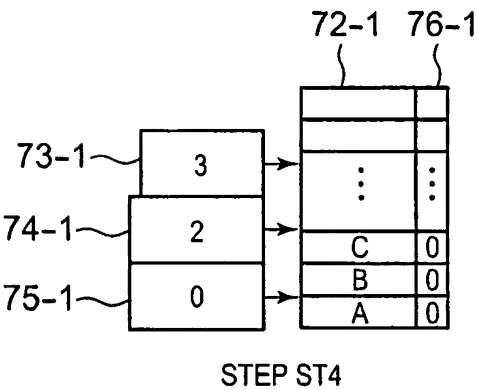


FIG. 8D

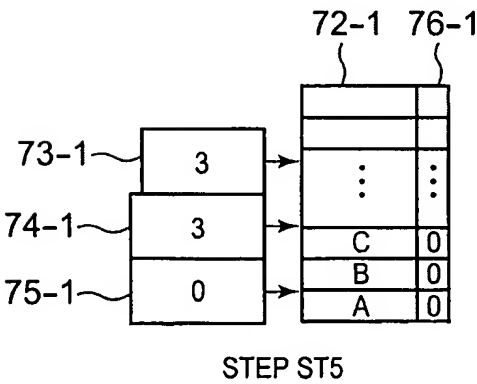


FIG. 8E

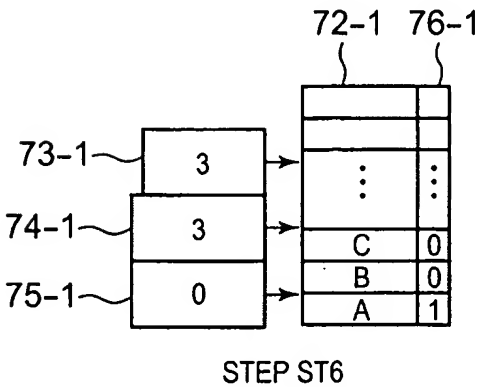


FIG. 8F

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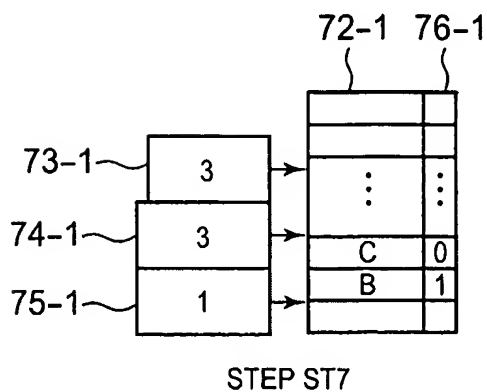


FIG. 8G

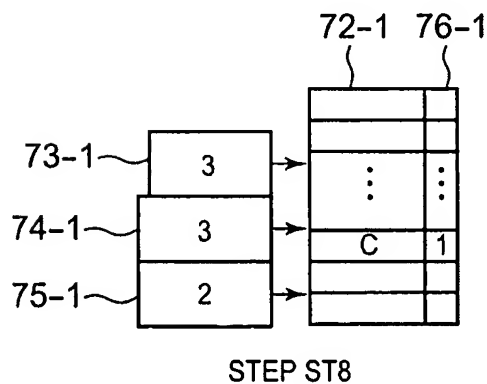


FIG. 8H

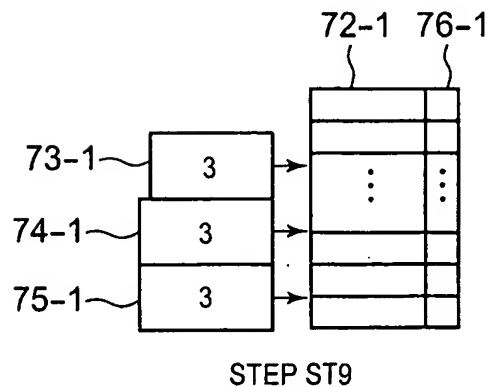


FIG. 8I

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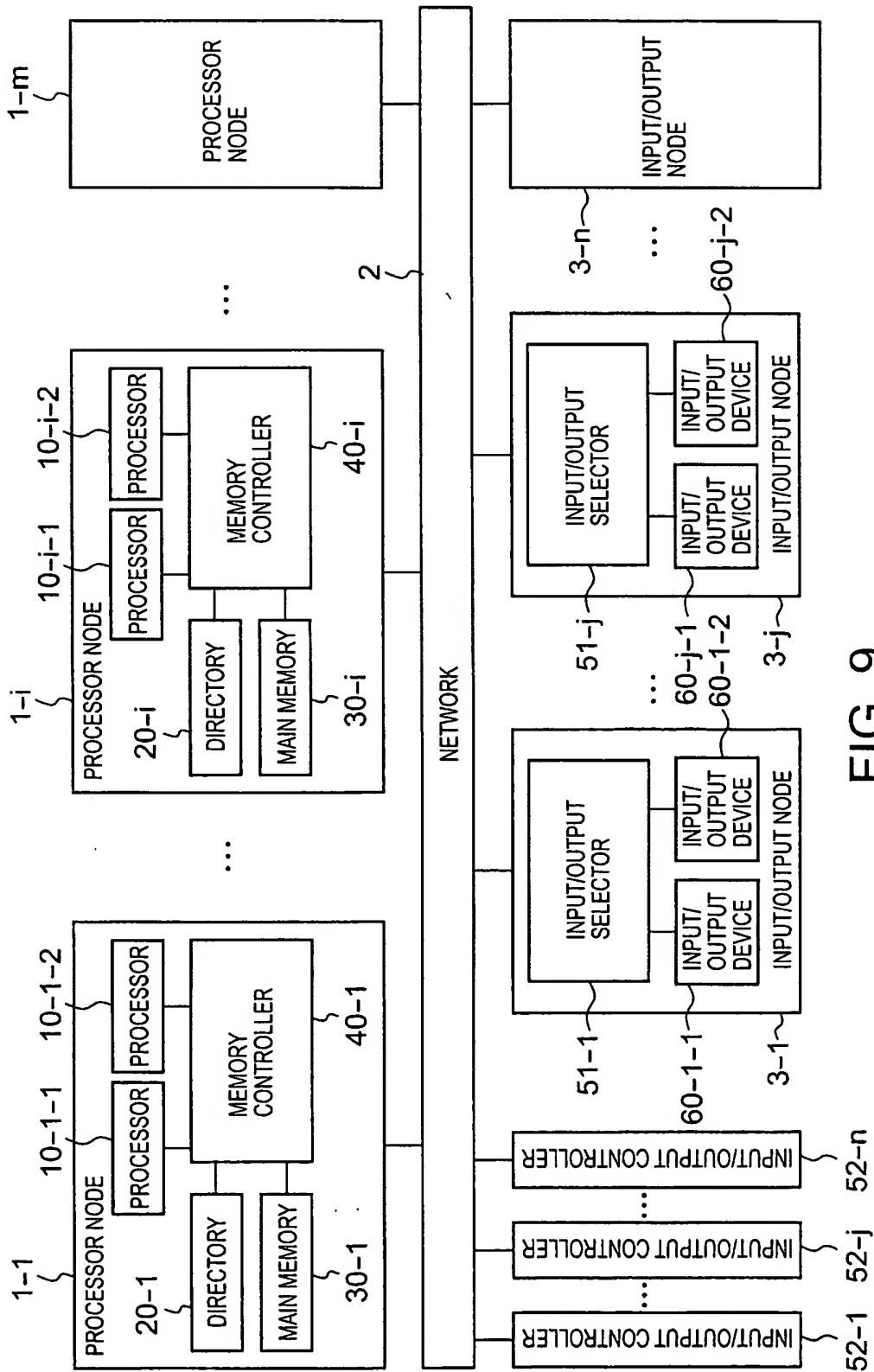


FIG. 9

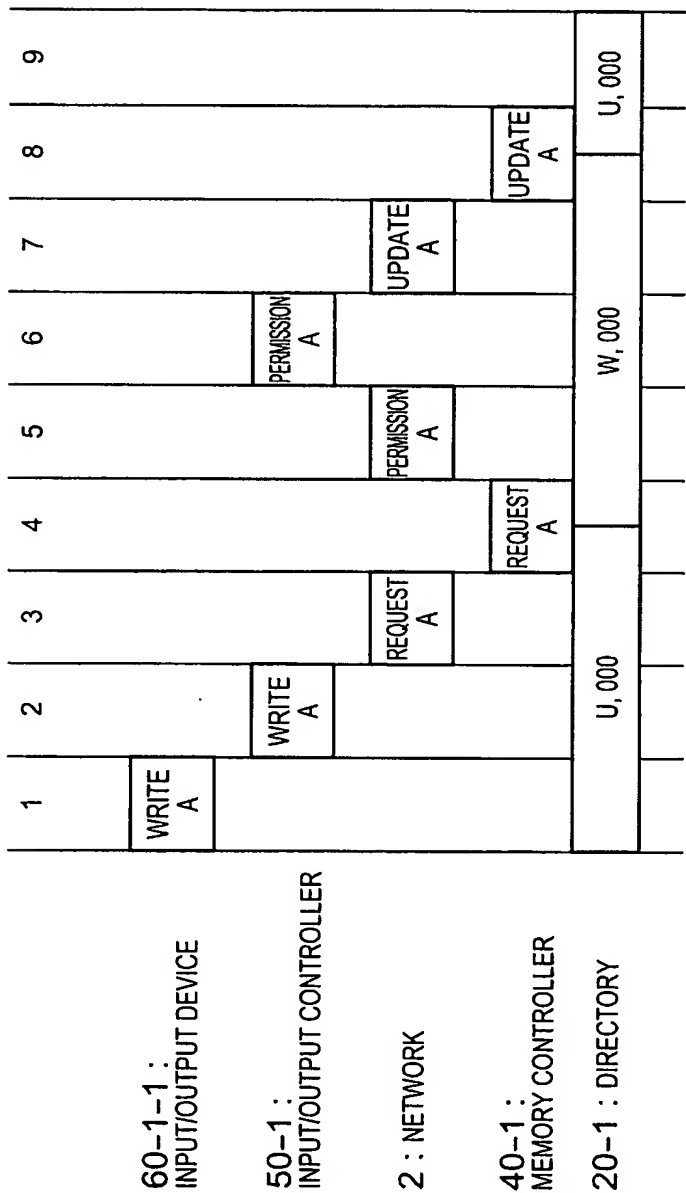


FIG. 10

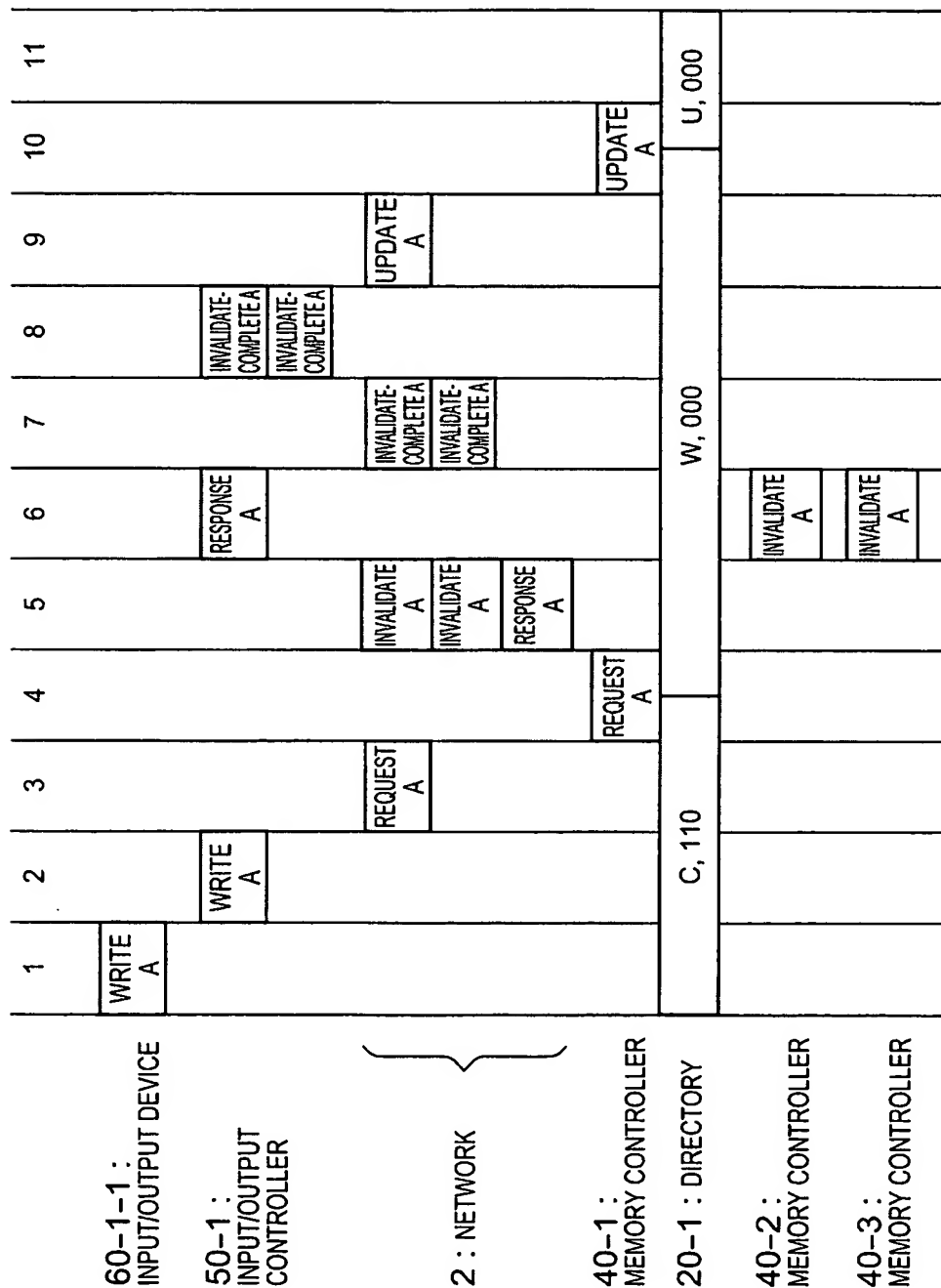


FIG. 11

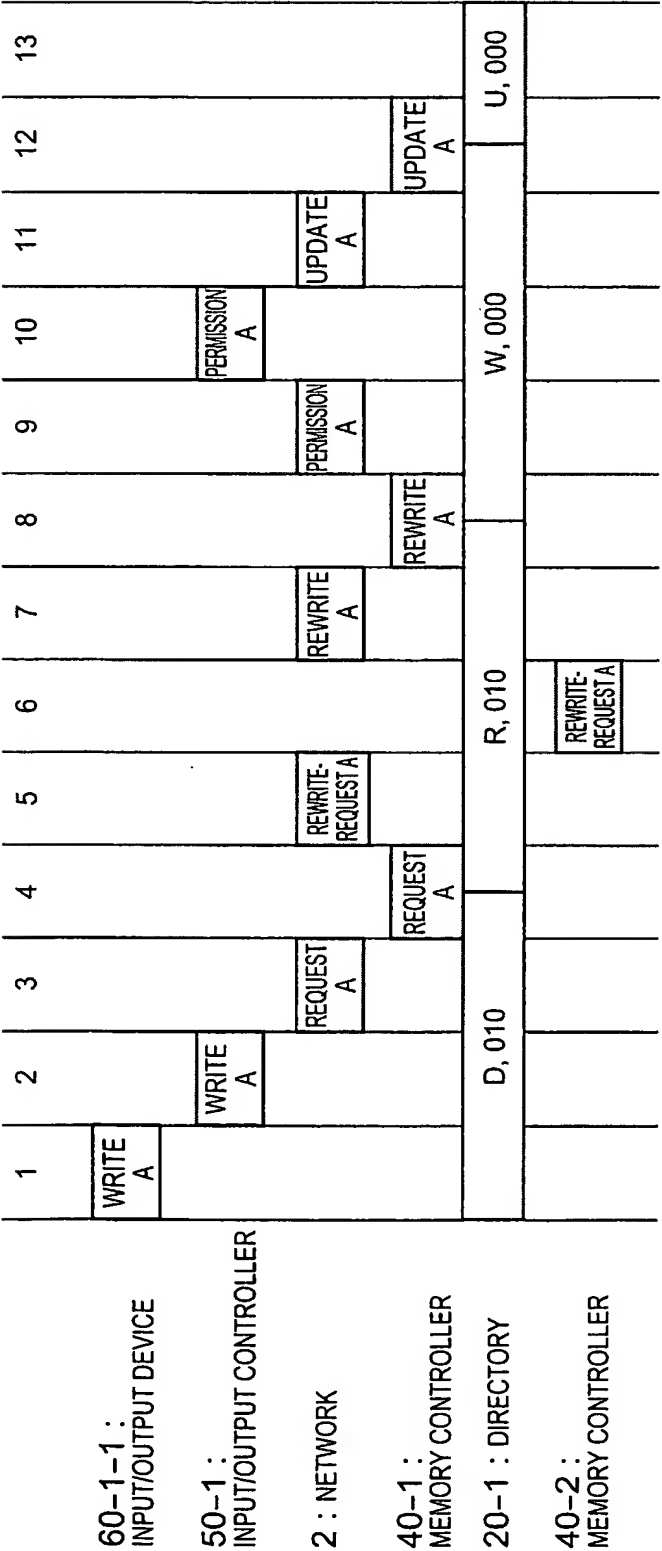


FIG. 12

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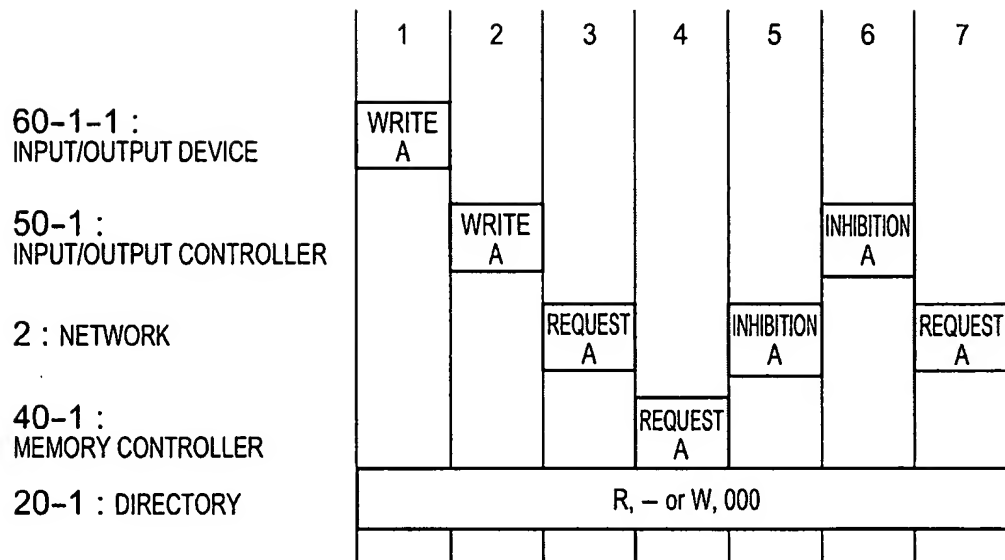


FIG. 13

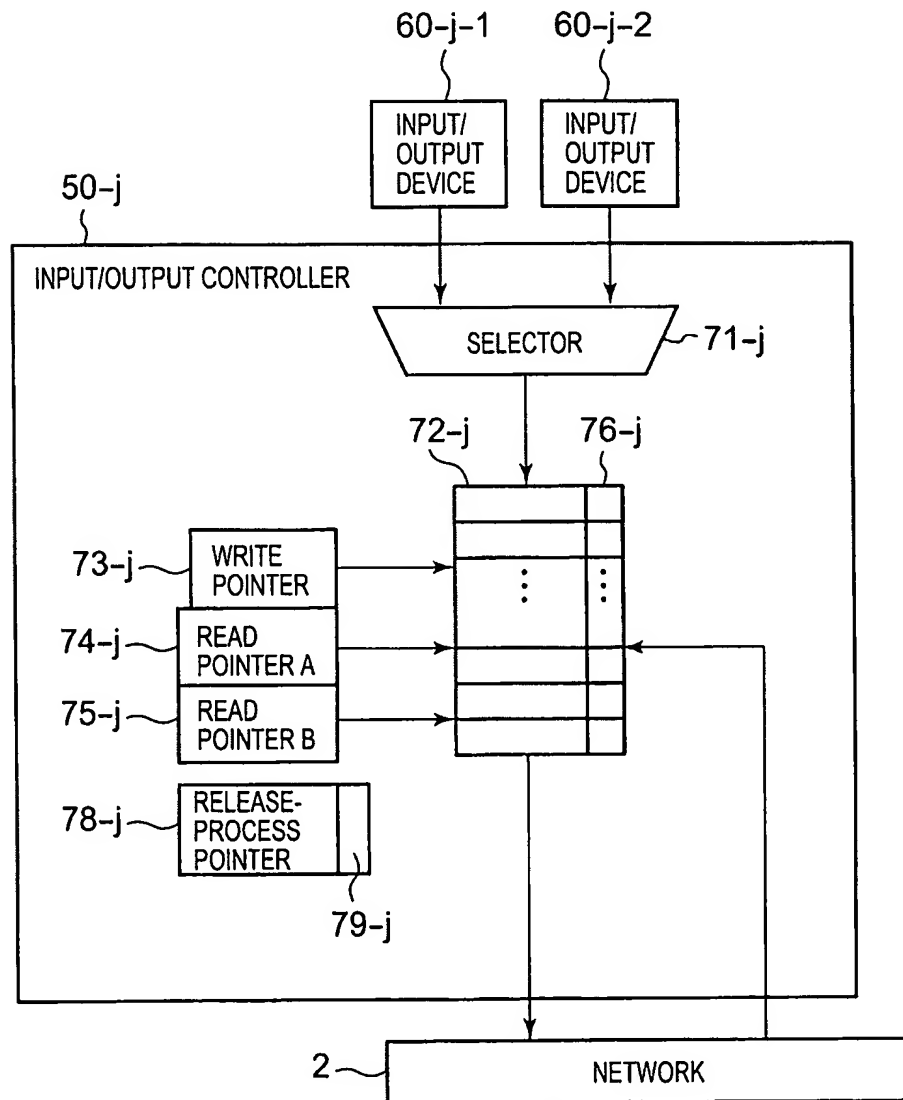


FIG. 14